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From: Jacqueline J. Garner  
Texas Instruments Incorporated  
Facsimile: 972-917-4418  
Phone: 214 532-9348

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## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

## In re the Application of:

	Jong Shik Yoon, et al.	Docket No.:	TI-37043
Serial No:	10/825,343	Filed:	04/15/2004
Art Unit:	2813	Examiner:	David S. Blum
Customer No.:	23494	Conf. No.:	8658
Title:	Minimizing Transistor Variations Due to Shallow Trench Isolation Stress		

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## FACSIMILE COVER SHEET

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<input type="checkbox"/> DIVISIONAL APP'N	
NAME OF INVENTOR(S): Jong Shik Yoon, et al.	
RECEIPT DATE & SERIAL NO.: Serial No.: 10/825,343	
TITLE OF INVENTION: Minimizing Transistor Variations Due to Shallow Trench Isolation Stress	
TI FILE NO.: <b>TI-37043</b>	DEPOSIT ACCT. NO.: 20-0668
FAXED: 3/2/2006	
DUE: 3/15/2006	
ATTY/SECY: JJG/ms	

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Texas Instruments Incorporated  
PO Box 655474, M/S 3999  
Dallas, TX 75265

mkf

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ELECTION

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450


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 Mariama Smith	<u>3-2-06</u> Date

Dear Sir:

With respect to the Restriction Requirement mailed on 2/15/2006, the Examiner has restricted the instant application to the invention of Group I (Claims 1-7), or Group II (Claims 8-17), or Group III (Claims 18-20). In light of this, Applicants elect to pursue Group I, (Claims 1-7) without traverse.

The Examiner further required restriction between the species of Claim 6 and the species of Claim 7. Applicants elect the species of Claim 6, Claims 1-5 being generic.

Respectfully submitted,

  
Jacqueline J. Garner  
Attorney for Applicants  
Reg. No. 36,144

Texas Instruments Incorporated  
P.O. Box 655474, MS 3999  
Dallas, TX 75265  
(214) 532-9348